

## 100V N-Channel Enhancement Mode Power MOSFET

## Description

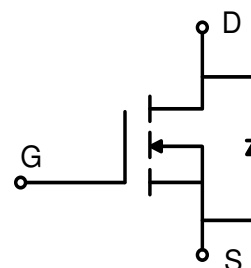
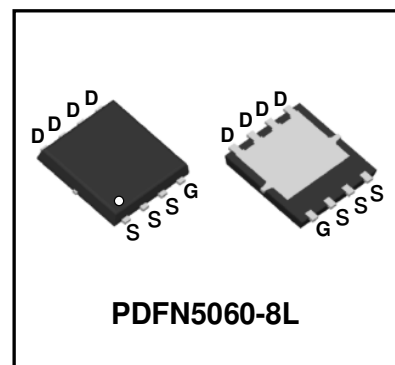
WMB060N10LGS uses Wayon's 2<sup>nd</sup> generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

## Features

- $V_{DS} = 100V$ ,  $I_D = 95A$   
 $R_{DS(on)} < 6m\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(on)} < 9m\Omega$  @  $V_{GS} = 4.5V$
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed

## Applications

- Power Management Switches
- DC/DC Converter
- LED Backlighting

Absolute Maximum Ratings ( $T_c = 25^\circ C$ , unless otherwise noted)

| Parameter                                        |                     | Symbol         | Value      | Unit       |
|--------------------------------------------------|---------------------|----------------|------------|------------|
| Drain-Source Voltage                             |                     | $V_{DS}$       | 100        | V          |
| Gate-Source Voltage                              |                     | $V_{GS}$       | $\pm 20$   | V          |
| Continuous Drain Current                         | $T_C = 25^\circ C$  | $I_D$          | 95         | A          |
|                                                  | $T_C = 100^\circ C$ |                | 60         |            |
| Pulsed Drain Current <sup>4</sup>                |                     | $I_{DM}$       | 380        | A          |
| Single Pulse Avalanche Energy <sup>3</sup>       |                     | <b>EAS</b>     | 205        | mJ         |
| Total Power Dissipation                          | $T_C = 25^\circ C$  | $P_D$          | 113.6      | W          |
| Operating Junction and Storage Temperature Range |                     | $T_J, T_{STG}$ | -55 to 150 | $^\circ C$ |

## Thermal Characteristics

| Parameter                                                | Symbol          | Value | Unit         |
|----------------------------------------------------------|-----------------|-------|--------------|
| Thermal Resistance from Junction-to-Ambient <sup>1</sup> | $R_{\theta JA}$ | 58    | $^\circ C/W$ |
| Thermal Resistance from Junction-to-Case                 | $R_{\theta JC}$ | 1.1   | $^\circ C/W$ |

**Electrical Characteristics** ( $T_c = 25^\circ\text{C}$ , unless otherwise noted)

| Parameter                                |                       | Symbol               | Test Conditions                                                                       | Min. | Typ. | Max. | Unit |
|------------------------------------------|-----------------------|----------------------|---------------------------------------------------------------------------------------|------|------|------|------|
| Static Characteristics                   |                       |                      |                                                                                       |      |      |      |      |
| Drain-Source Breakdown Voltage           |                       | V <sub>(BR)DSS</sub> | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA                                          | 100  | -    | -    | V    |
| Gate-body Leakage current                |                       | I <sub>GSS</sub>     | V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V                                          | -    | -    | ±100 | nA   |
| Zero Gate Voltage Drain Current          | T <sub>J</sub> =25°C  | I <sub>DSS</sub>     | V <sub>DS</sub> =100V, V <sub>GS</sub> = 0V                                           | -    | -    | 1    | μA   |
|                                          | T <sub>J</sub> =100°C |                      |                                                                                       | -    | -    | 100  |      |
| Gate-Threshold Voltage                   |                       | V <sub>GS(th)</sub>  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA                            | 1.2  | 1.8  | 2.5  | V    |
| Drain-Source on-Resistance <sup>2</sup>  |                       | R <sub>DS(on)</sub>  | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A                                           | -    | 4.5  | 6    | mΩ   |
|                                          |                       |                      | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 15A                                          | -    | 6.6  | 9    |      |
| Dynamic Characteristics                  |                       |                      |                                                                                       |      |      |      |      |
| Input Capacitance                        |                       | C <sub>iss</sub>     | V <sub>DS</sub> = 50V, V <sub>GS</sub> =0V, f =1MHz                                   | -    | 4400 | -    | pF   |
| Output Capacitance                       |                       | C <sub>oss</sub>     |                                                                                       | -    | 645  | -    |      |
| Reverse Transfer Capacitance             |                       | C <sub>rss</sub>     |                                                                                       | -    | 20   | -    |      |
| Switching Characteristics                |                       |                      |                                                                                       |      |      |      |      |
| Gate Resistance                          |                       | R <sub>g</sub>       | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f =1MHz                                   | -    | 1.7  | -    | Ω    |
| Total Gate Charge                        |                       | Q <sub>g</sub>       | V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> =20A                     | -    | 75   | -    | nC   |
| Gate-Source Charge                       |                       | Q <sub>gs</sub>      |                                                                                       | -    | 17   | -    |      |
| Gate-Drain Charge                        |                       | Q <sub>gd</sub>      |                                                                                       | -    | 13   | -    |      |
| Turn-on Delay Time                       |                       | t <sub>d(on)</sub>   | V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 20A | -    | 15.4 | -    | ns   |
| Rise Time                                |                       | t <sub>r</sub>       |                                                                                       | -    | 13   | -    |      |
| Turn-off Delay Time                      |                       | t <sub>d(off)</sub>  |                                                                                       | -    | 34   | -    |      |
| Fall Time                                |                       | t <sub>f</sub>       |                                                                                       | -    | 6.2  | -    |      |
| Drain-Source Body Diode Characteristics  |                       |                      |                                                                                       |      |      |      |      |
| Diode Forward Voltage <sup>2</sup>       |                       | V <sub>SD</sub>      | I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V                                            | -    | -    | 1.2  | V    |
| Continuous Source Current <sup>1,5</sup> |                       | I <sub>s</sub>       | V <sub>G</sub> =V <sub>D</sub> =0V , Force Current                                    | -    | -    | 95   | A    |
| Body Diode Reverse Recovery Time         |                       | t <sub>rr</sub>      | I <sub>F</sub> = 20A, dI/dt=100A/μs                                                   | -    | 55   | -    | ns   |
| Body Diode Reverse Recovery Charge       |                       | Q <sub>rr</sub>      |                                                                                       | -    | 101  | -    | nC   |

## Notes:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is  $V_{DD} = 50V, V_{GS} = 10V, L = 0.4mH, I_{AS} = 32A$
4. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)} = 150^\circ\text{C}$ .
5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

## Typical Characteristics

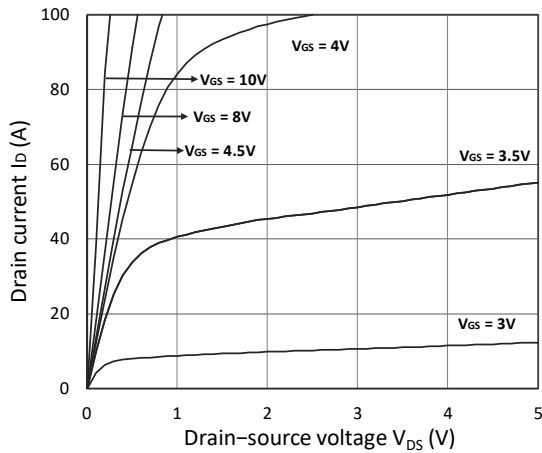


Figure 1. Output Characteristics

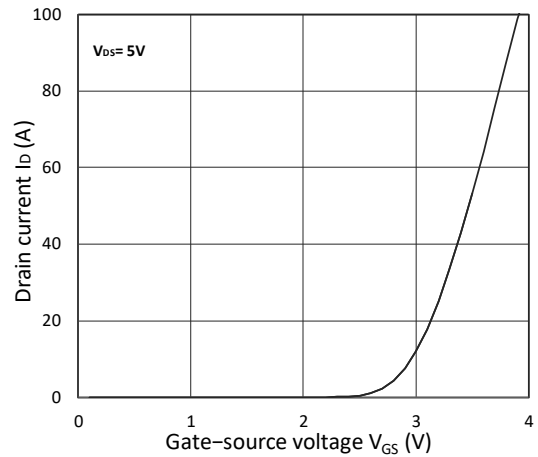


Figure 2. Transfer Characteristics

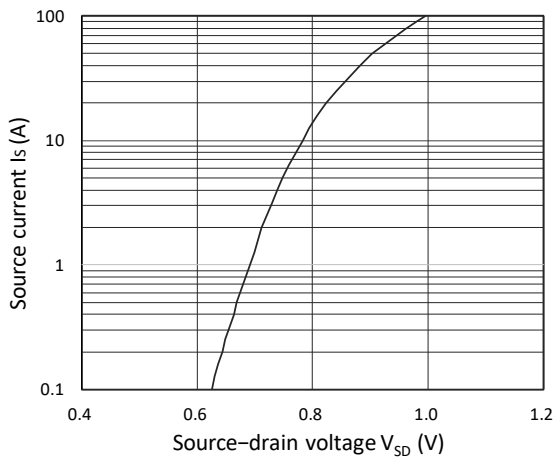
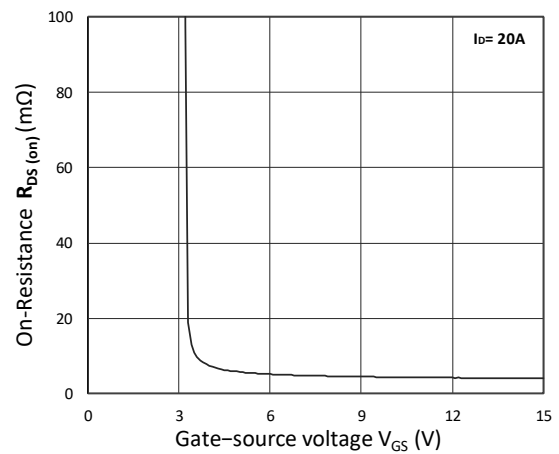
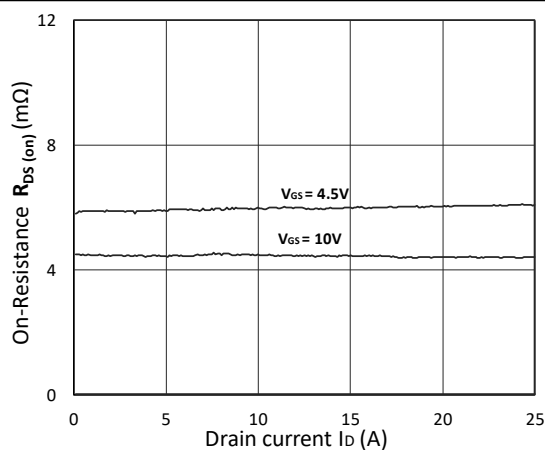
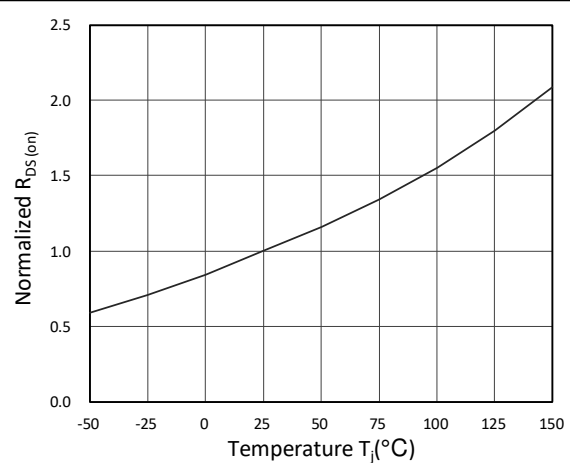


Figure 3. Forward Characteristics of Reverse

Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$ Figure 5.  $R_{DS(ON)}$  vs.  $I_D$ Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

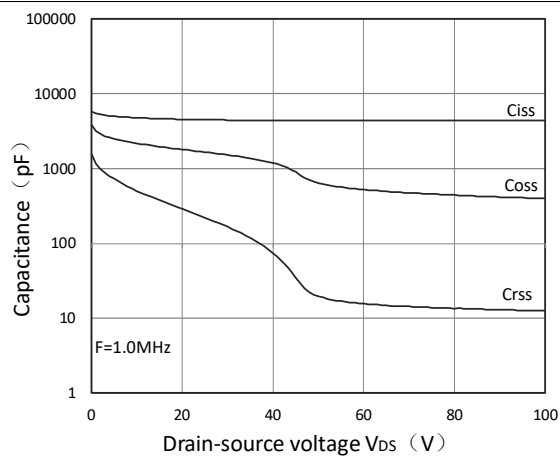


Figure 7. Capacitance Characteristics

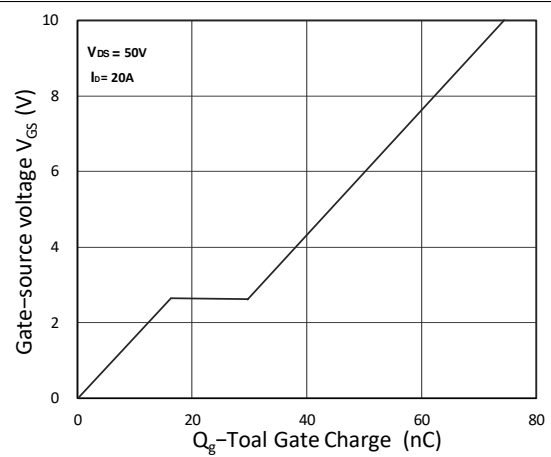


Figure 8. Gate Charge Characteristics

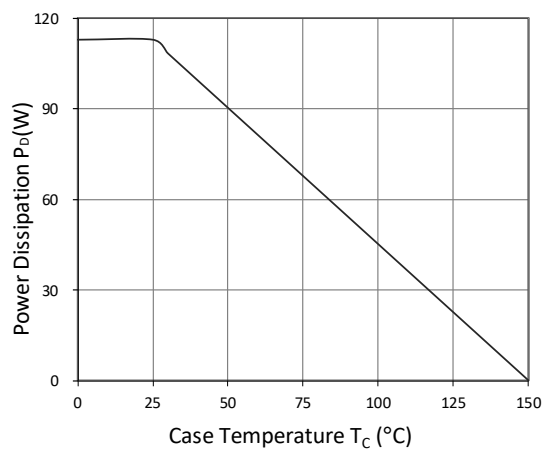


Figure 9. Power Dissipation

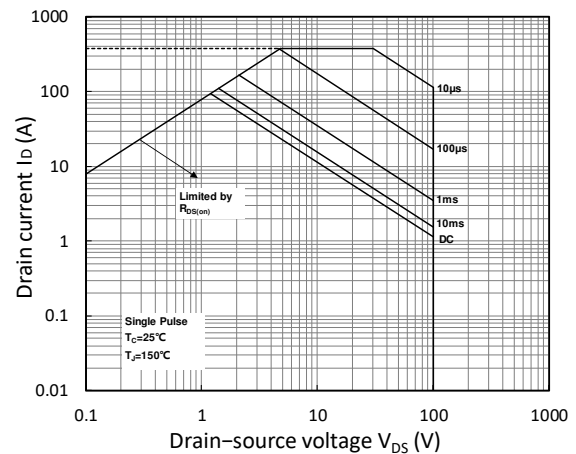


Figure 10. Safe Operating Area

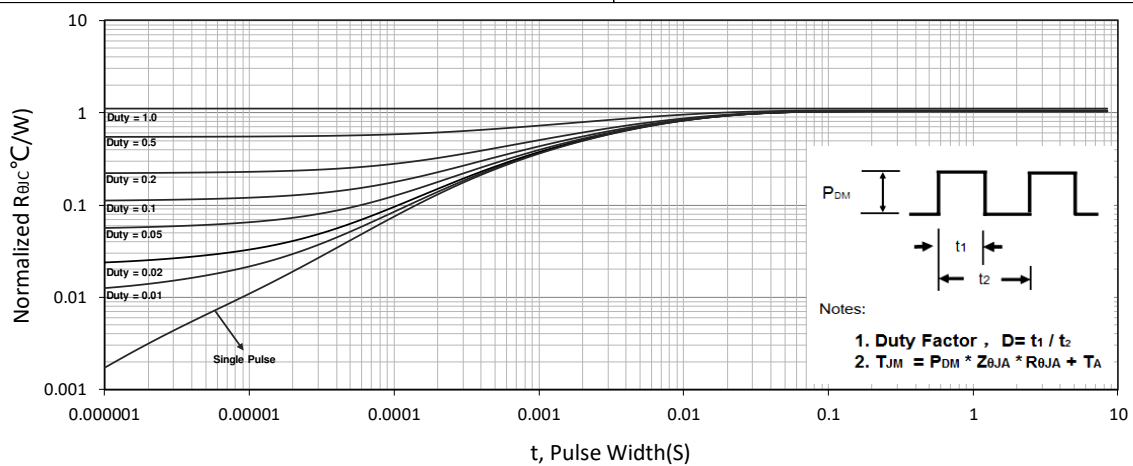


Figure 11. Normalized Maximum Transient Thermal Impedance

## Test Circuit

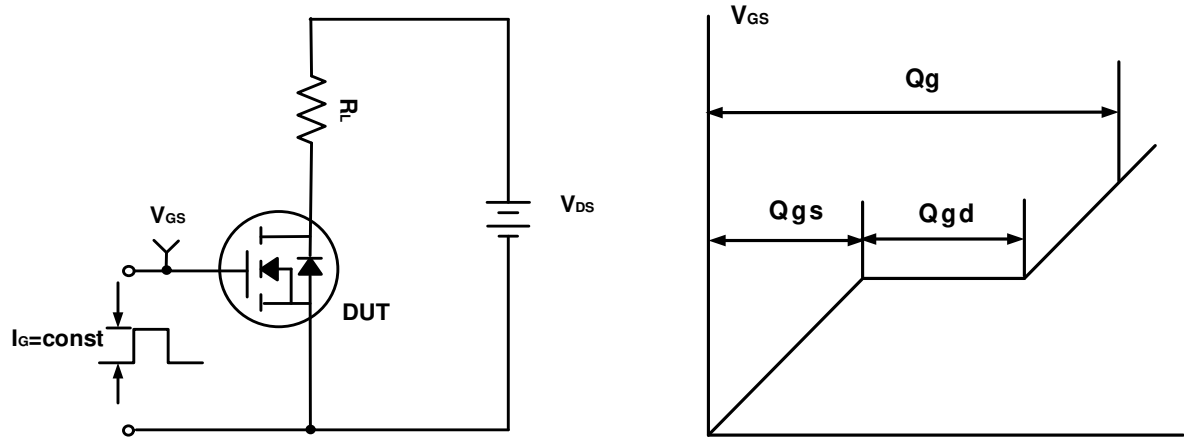


Figure A. Gate Charge Test Circuit &amp; Waveforms

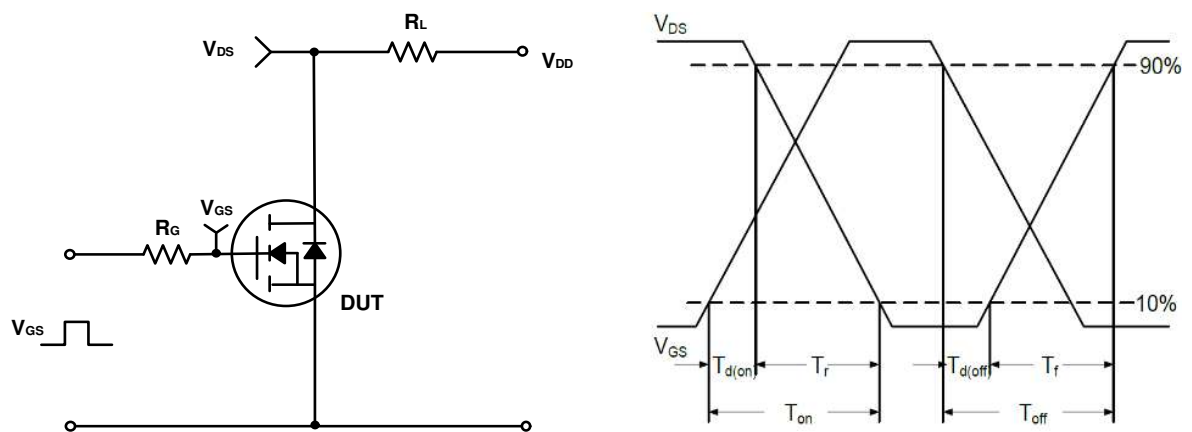


Figure B. Switching Test Circuit &amp; Waveforms

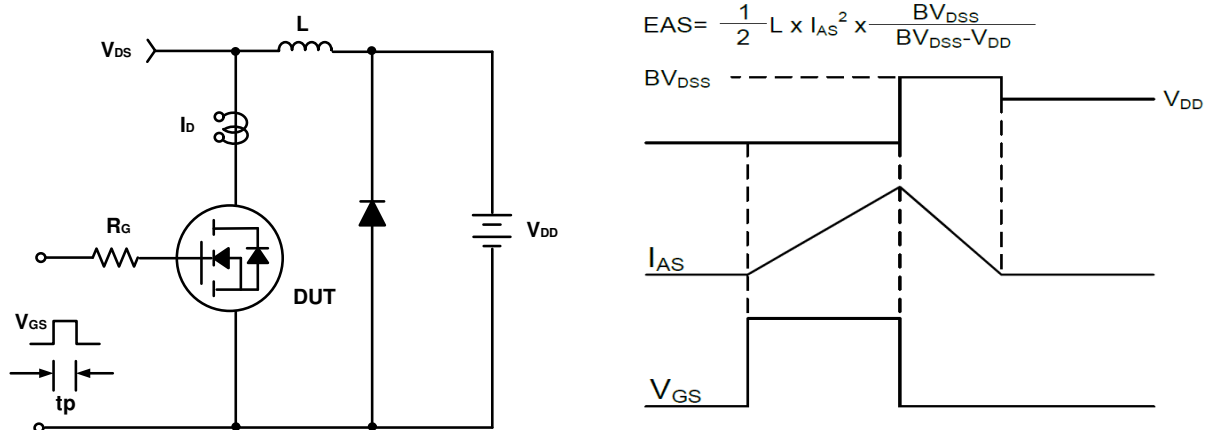
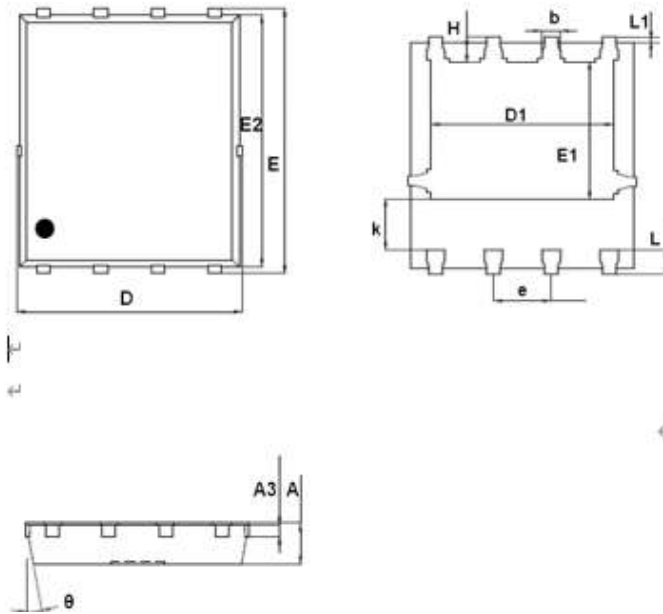


Figure C. Unclamped Inductive Switching Circuit &amp; Waveforms

## Mechanical Dimensions for PDFN5060-8L

## COMMON DIMENSIONS

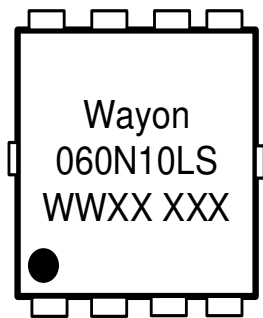


| SYMBOL   | MM      |      |
|----------|---------|------|
|          | MIN     | MAX  |
| A        | 0.9     | 1.2  |
| A3       | 0.15    | 0.35 |
| D        | 4.8     | 5.4  |
| E        | 5.9     | 6.35 |
| D1       | 3.61    | 4.31 |
| E1       | 3.3     | 3.92 |
| E2       | 5.5     | 6.06 |
| k        | 1.1     | -    |
| b        | 0.3     | 0.51 |
| e        | 1.27BSC |      |
| L        | 0.38    | 0.71 |
| L1       | 0.05    | 0.36 |
| H        | 0.38    | 0.71 |
| $\theta$ | 0°      | 12°  |

## Ordering Information

| Part         | Package     | Marking  | Packing method |
|--------------|-------------|----------|----------------|
| WMB060N10LGS | PDFN5060-8L | 060N10LS | Tape and Reel  |

## Marking Information



060N10LS = Device code

WWXX XXX= Date code

## Contact Information

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