

30V N-Channel Enhancement Mode Power MOSFET

Description

WMQ28N03T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = 30V$, $I_D = 28A$
 $R_{DS(on)} < 18m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 30m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- Low Gate Charge
- Advanced High Cell Density Trench Technology
- 100% EAS Guaranteed

Applications

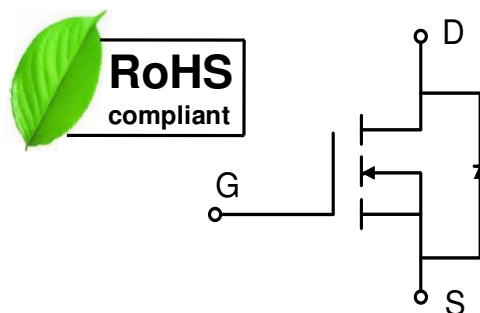
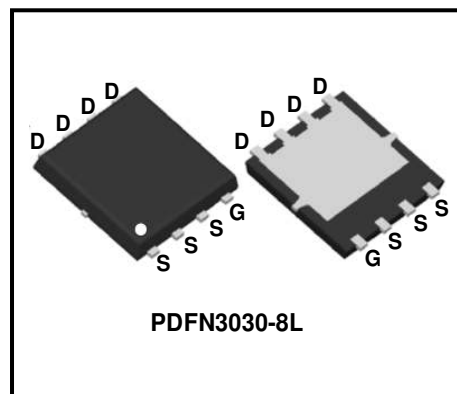
- Power Management Switches
- DC/DC Converter

Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ¹	$T_C = 25^\circ C$	I_D	28	A
	$T_C = 100^\circ C$		18	
Pulsed Drain Current ²		I_{DM}	54	A
Single Pulse Avalanche Energy ³		EAS	12.8	mJ
Avalanche Current		I_{AS}	16	A
Total Power Dissipation ⁴		P_D	21	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	$R_{\theta JA}$	74	$^\circ C/W$
Thermal Resistance from Junction-to-Case ¹	$R_{\theta JC}$	5.95	$^\circ C/W$



Electrical Characteristics $T_c = 25^\circ\text{C}$, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30	-	-	V
Gate-Body Leakage Current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V	-	-	1	μA
	T _J =55°C			-	-	5	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.75	2.5	V
Drain-Source On-Resistance ²		R _{DS(on)}	V _{GS} = 10V, I _D = 10A	-	14	18	mΩ
			V _{GS} = 4.5V, I _D = 5A	-	20	30	
Forward Transconductance ²		g _{fs}	V _{DS} = 5V, I _D = 1A	-	4.6	-	S
Dynamic Characteristics							
Input Capacitance		C _{iss}	V _{DS} = 15V, V _{GS} =0V, f =1MHz	-	500	-	pF
Output Capacitance		C _{oss}		-	75	-	
Reverse Transfer Capacitance		C _{rss}		-	51	-	
Switching Characteristics							
Gate Resistance		R _g	V _{DS} = 0V, V _{GS} =0V, f =1MHz	-	2.5	-	Ω
Total Gate Charge		Q _g	V _{GS} = 4.5V, V _{DS} = 20V, I _D = 10A	-	7.3	-	nC
Gate-Source Charge		Q _{gs}		-	1.5	-	
Gate-Drain Charge		Q _{gd}		-	2.3	-	
Turn-On Delay Time		t _{d(on)}	V _{GS} =10V, V _{DD} = 12V, R _G = 3.3Ω, I _D = 5A	-	4.2	-	nS
Rise Time		t _r		-	9.9	-	
Turn-Off Delay Time		t _{d(off)}		-	15.8	-	
Fall Time		t _f		-	6.2	-	
Drain-source body diode Characteristics							
Diode Forward Voltage ²		V _{SD}	I _S = 1A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current ^{1,5}		I _S	V _G =V _D =0V , Force Current	-	-	28	A

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 0.1mH, I_{AS} = 16A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

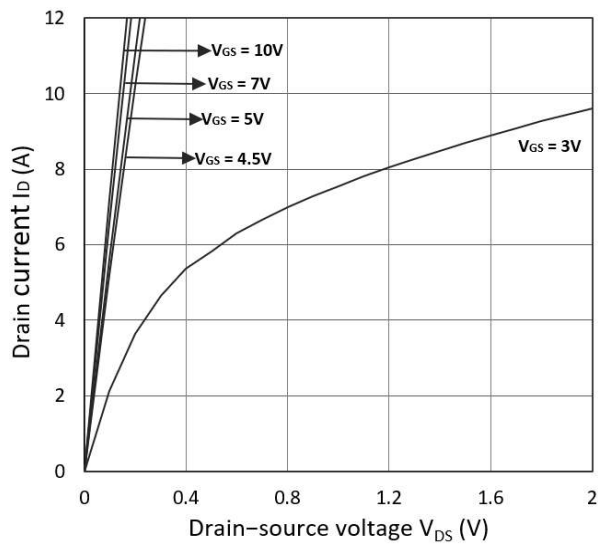


Figure 1. Output Characteristics

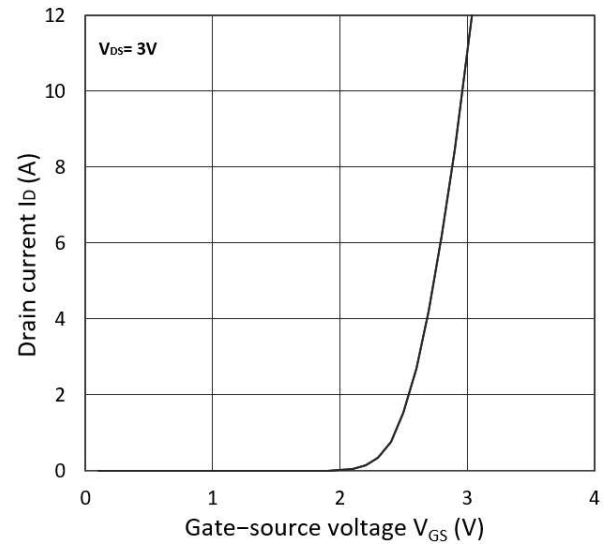


Figure 2. Transfer Characteristics

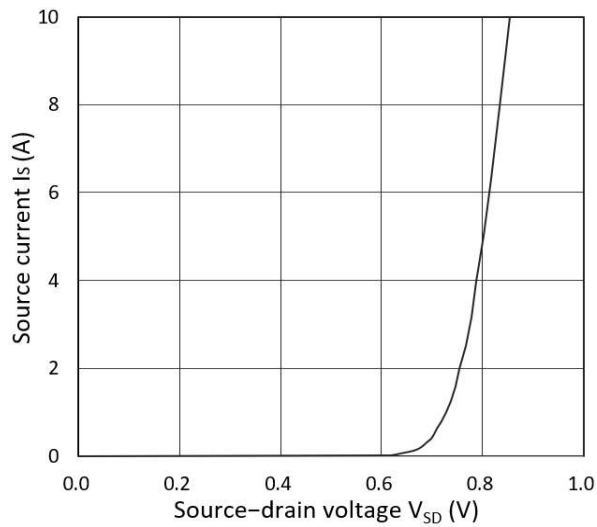


Figure 3. Forward Characteristics of Reverse

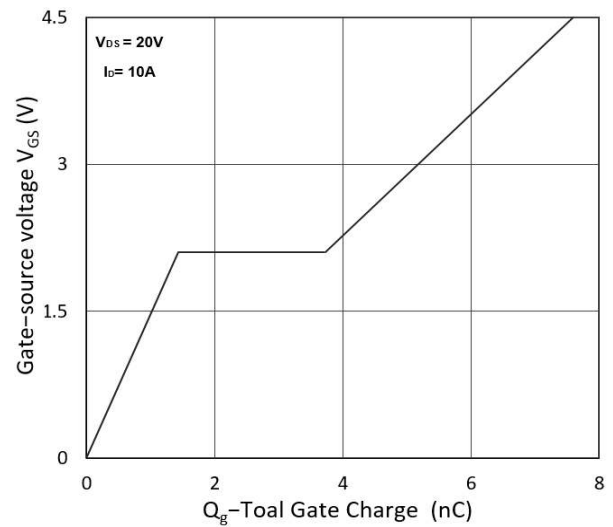
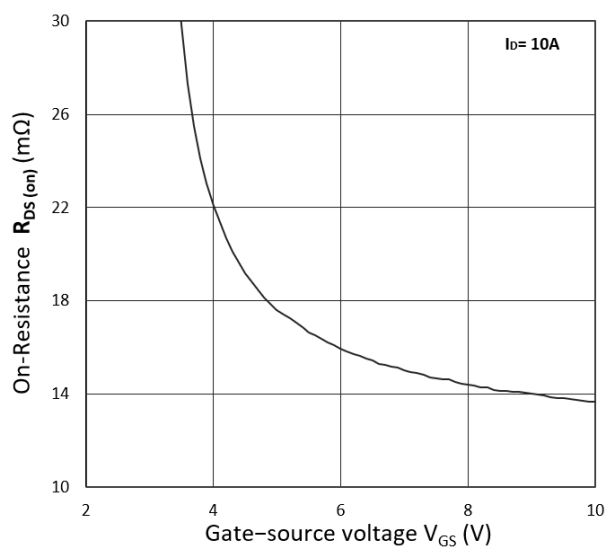
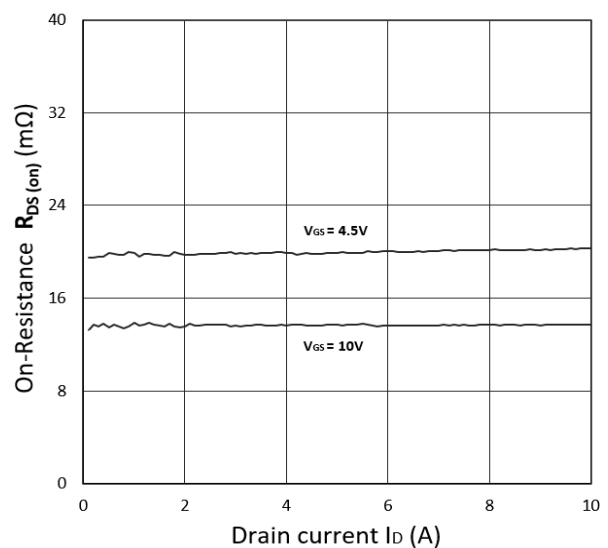


Figure 4. Gate Charge Characteristics

Figure 5. $R_{DS(on)}$ vs. V_{GS} Figure 6. $R_{DS(on)}$ vs. I_D

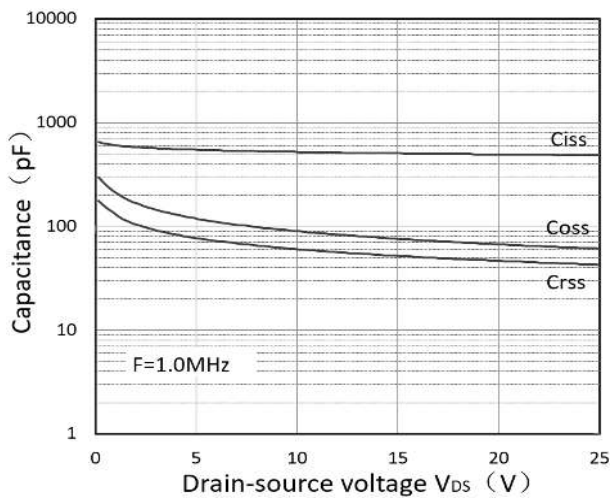


Figure 7. Capacitance Characteristics

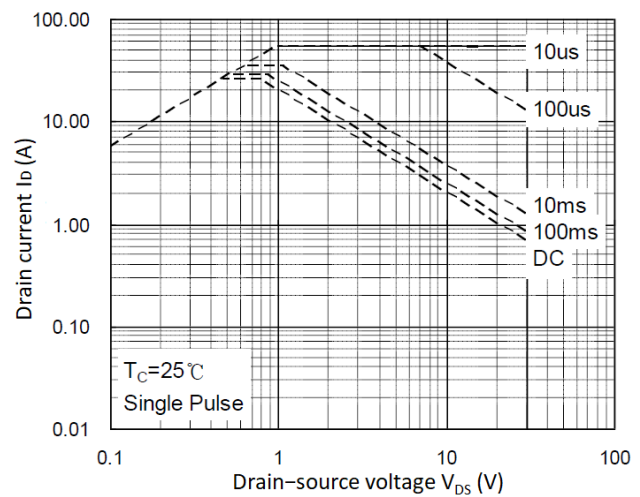


Figure 8. Safe Operating Area

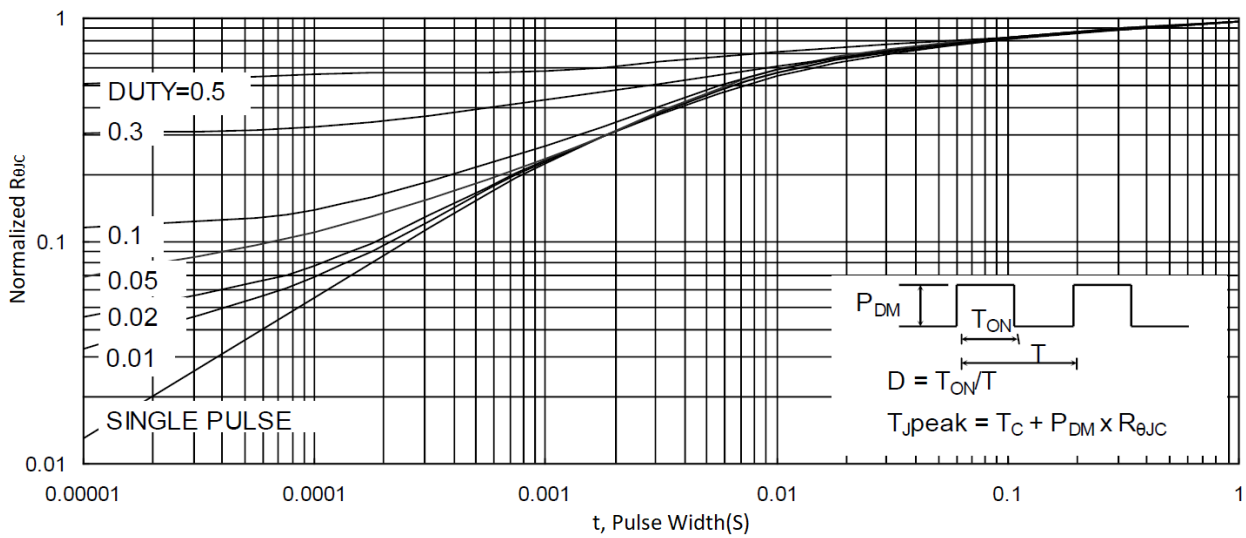


Figure 9. Normalized Maximum Transient Thermal Impedance

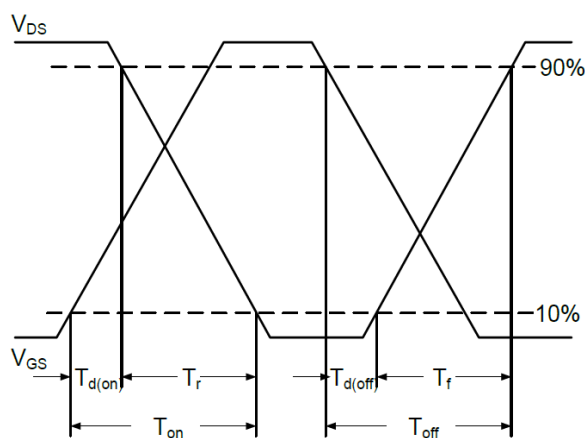
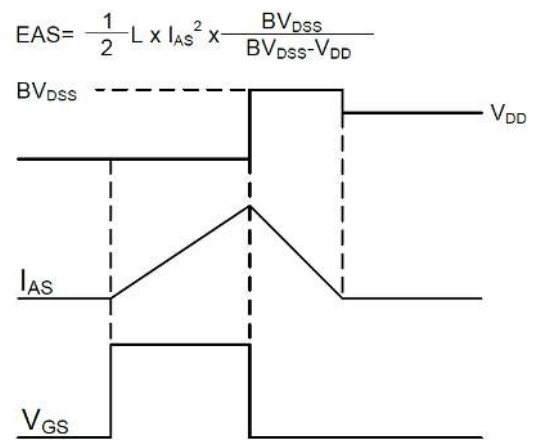
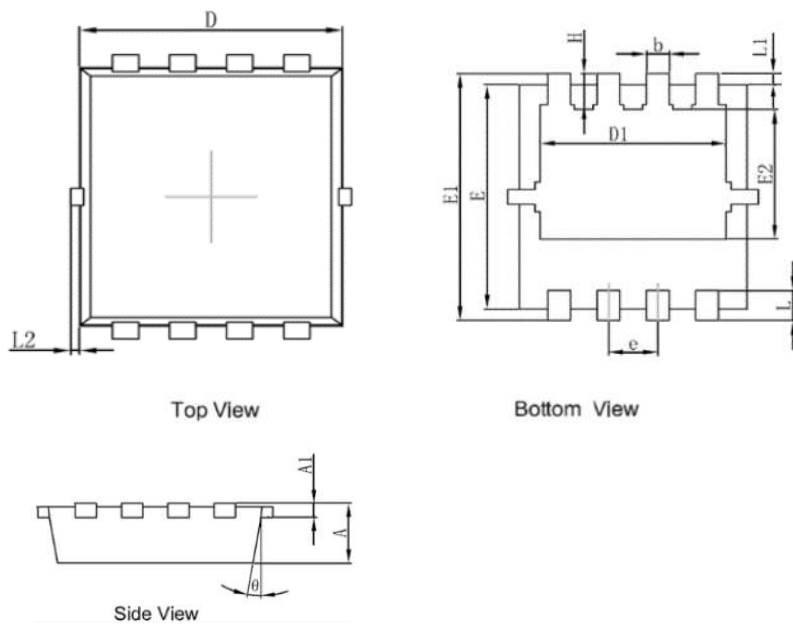


Figure 10. Switching Time Waveform

Figure 11. Unclamped Inductive Switching
Waveform

Mechanical Dimensions for PDFN3030-8L



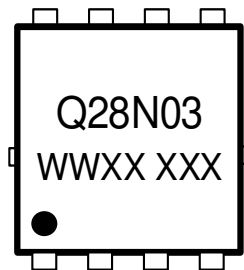
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.70	0.85
A1	0.10	0.25
D	2.90	3.25
D1	2.25	2.65
E	2.90	3.20
E1	3.10	3.45
E2	1.54	1.98
b	0.20	0.40
e	0.60	0.70
L	0.30	0.50
L1	0.13BSC	
L2	0.00	0.15
H	0.20	0.65
θ	0°	14°

Ordering Information

Part	Package	Marking	Packing method
WMQ28N03T1	PDFN3030-8L	Q28N03	Tape and Reel

Marking Information



Q28N03 = Device code
WWXX XXX= Date code

Contact Information

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